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Design of microwave lowpass filter based on log periodic zigzag DGS

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This contribution presents, for the first time, the design of microwave lowpass filter with ultrawide stopband and low insertion loss based on the log-periodic zig-zag defected ground structure (DGS); the design is specifically evolved from the principle of the log-periodic line antenna and the slotline based DGS. Based on the log-periodic property, studies show that ultra-wideband suppression can be realized by increasing the flare angle or the angle subtended by each element of the slotline DGS. The developed filter therefore shows low insertion loss of 0.7 dB within the passband, sharp transition between the passband and the stopband and ultra-wide suppression band from 2.61-26.5 GHz with a 20 dB suppression level. The DGS architecture is simple and easy to realize, and the design is thus useful in many microwave applications. The fabricated prototype filter validates the study, both from simulations and from measurements.

Keywords: log periodic, zigzag, slotline, defected ground structure (DGS), microwave filter

1 Introduction

In today's wireless communication and the increasing connectivity of things (internet of things), microwave filters and other related components are indispensable currently and in the nearfuture [1]-[4]. Compact size and high-performance low profile microstrip lowpass filters (LPFs) with wide stopband, good return loss and low insertion loss are in high demand for various wireless applications. This is due to the ability of such filters to suppress wideband noise and spurious signals. Also due to easy fabrication and the lower cost of microstrip based filters, it reduces cost whiles enhancing system performance. Many microstrip based filters have been produced till date, but it is important that the performance of such filters needs to be further improved for all related applications. Hence the continuous performance improvement of such filters being experienced including this contribution. LPFs are vital components that have found wide applications in todays wireless communication system for noise and intermodulation signals suppression.

In [5], complementary split ring resonators were used to design compact low insertion loss LPF with sharp transition between the passband and the stopband. In the design, the lowpass band insertion loss was good, but the suppression band is not wide enough. In [6], an accurate model of a rectangular bracket DGS was presented and further validated by cascaded two and three DGS cells. In [7], the hairpin shaped DGS was studied and further developed for microwave filter applications. The developed demonstrator achieved a compact size and wide stopband

suppression. In [8], Yagi shaped DGS was also exploited for microwave filter applications and demonstrated good performance. In [9], a miniaturized LPF with improved Koch Fractal DGS was proposed with sharp transition and ultra-wideband stopband as compared to the traditional dumbbell-shaped DGS filters. In [10], a compact microstrip LPF has been proposed with sharp roll-off and a wide stopband. It also characterized a high figure of merit with a compact size.

In general, LPFs with ultra-wideband suppression and sharp roll-off as well as low insertion loss within the passband are very much desired for many practical applications. In this contribution, a log-periodic zigzag slotline based DGS is reported, for the first time, for microstrip LPF applications. The developed filter features ultra-wide stopband and low insertion loss within the passband as well as sharp transition between the passband and the stopband. To validate the study, a prototype filter is fabricated and confirmed, both from simulations and measurements.

2 Log periodic zigzag DGS

The proposed DGS is based on the principles of the log periodic antenna design [11][15] and the slotline based technique. These slots are etched on the ground plane of a microwave substrate under a microstrip line. It is known that the log periodic antenna is a frequency independent antenna composed of resonant elements. It can achieve frequency independence by having its resonant elements staggered so that as frequency is varied, thus the function

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of the resonant element is transferred smoothly from one element to the next. For an antenna array made up of similar discrete elements, this implies that the physical dimensions of the elements should be scaled from one to the next such that the required frequency range is covered with elements having overlapping response [11]. In [12], a log periodic-Yagi-Uda array was studied where the principle of the log periodic was applied to the Yagi-Uda technique to realize the bandwidth enhancement of the basic Yagi-Uda antenna. The antenna in [12] was made of individual cells each resembling the basic Yagi-Uda but each cell has different size by a geometric constant, where the driven element was fed by a common transmission line. This shows the versatility in application of the log-periodic structure and, in principle, to various microwave applications. In view of such type of application, this contribution proposes a DGS structure based on the log-periodic array to develop the microwave lowpass filter. For the general case of the log periodic antenna, the resonant elements are parallel to each other but for this proposed log-periodic DGS, the resonant elements etched on the ground plane have been arranged in a zig-zag fashion (slanted resonant elements) with some small gap between the tip of each vertex of the zig-zag elements, as shown in Fig. 1. Such a fashion constitutes a slot array under a certain periodicity.

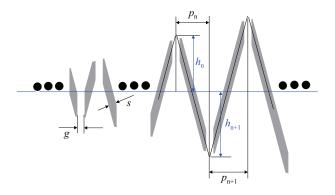


Fig. 1. The proposed slotline based log periodic zigzag DGS

It is shown in Fig. 1 that referred to the horizontal axis (corresponding to the central line, which is also the direction of placed microstrip line), the height from the vertex of each zig-zag to the central line is h_n for the $n^{\rm th}$ slanted slot, where n takes on all positive or negative integral values. Similarly, it is marked as h_{n+1} for the $(n+1)^{\rm th}$ slot. The spacing between two adjacent vertexes is p_n related to the $n^{\rm th}$ slot. These two parameters are of the geometric ratio or called scale factor that is generally given by: for the height ratio

$$\frac{1}{\tau_h} = \frac{h_{n+1}}{h_n} \,, \tag{1}$$

and for the spacing ratio

$$\frac{1}{\tau_p} = \frac{p_{n+1}}{p_n} \,, \tag{2}$$

where both ratios are less than 1.

To separate the slots between two adjacent vertexes of the zigzag, a small gap that is marked as g is introduced at the tip of each vertex as shown in Fig. 1. Notice that if carried to the limit, the array would converge to the point 0 on the left when n approaches $-\infty$, and becomes infinite in extent on the right as n arrives ∞ .

For such a kind of DGS, the height h_n and spacing p_n co-determine the length of the related slot and further determine its resonance. On the other hand, to be useful as a practical application, it must be possible to truncate the slot array so that at all frequencies above some lower limit, the truncated array performs like the infinite structure. The lower resonance is thus determined by the longest slot while the shortest one determines the upper resonance. The last parameter is related to the characteristic impedance of the slot that is marked as slot width s in Fig. 1. This characteristic impedance could influence the impedance matching of the microstrip transmission line placed above the proposed DGS structure in this study.

3 Developing microstrip filter based on the proposed DGS

To demonstrate the potential application of the proposed DGS, a microstrip LPF is developed here. The filter is designed on a microwave substrate having a relative permittivity of 9.6 and a thickness of 0.8 mm. The dedicated filter has a cut-off frequency of $f_c = 2$ GHz and the expected suppression band is up to 24 GHz, namely $12 f_c$, thus a very wideband suppression. On the abovementioned substrate, the longest slot should have a length of approximately 24 mm when the slot functions as a half-wavelength resonator at f_c . Correspondingly, in this study, we set its height h_n to be 16 mm. Meanwhile, we also set $\tau_h = \tau_p = \tau$ to simplify the design. The rest parameter could be the initial periodicity that would be marked as p_{n-1} based on Fig. 1. The scale factor τ is sweeping studied here and referred to the related studies, it is sweeping from 0.7 to 0.9 in this work. The shortest slot, of course, determined by the upper suppression margin, can thus be determined by the scale factor τ and the initial height h_n as well as the initial periodicity p_{n-1} under a certain periodicity number n. The longest and shortest slots are, as mentioned before, formulating the truncation for such an infinite slot array. The microstrip line used in this study is a standard 50 Ω transmission line placed on the substrate above the proposed DGS structure. Now considering that, the initial slot is serving as a half-wavelength resonator with a fundamental resonance of 2 GHz, and the initial height $h_n = 16$ mm as well as a sweeping scale factor $\tau = 0.7 - 0.9$, the initial periodicity p_{n-1} is set as 8 mm and sweeping varied from 7 mm to 9 mm. With these initial values, the studied filter can be

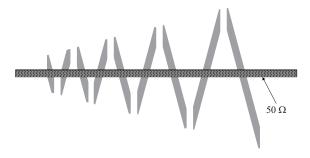


Fig. 2. Layout of the proposed log periodic zigzag DGS prototype filterwhere the gray section denotes the patterned ground while the shaded section represents the microstrip transmission line



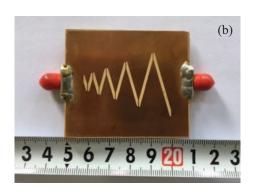


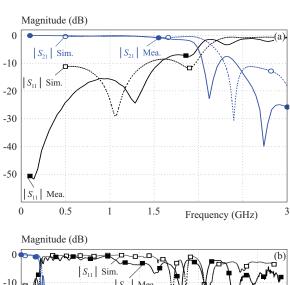
Fig. 3. Photograph of the fabricated logperiodic DGS prototype filterafront sidebback side

developed with the use of full-wave electromagnetic (EM) simulations, here is from Ansoft Ensemble 8.0. Figure 2 shows the layout of the studied prototype filter.

After sweeping studies and full-wave EM optimizations, the dedicated filter is developed under a suppression level of 20 dB in the suppression band and an in-band return loss of 10 dB. Results show a total of 10 sections (namely the periodicity number is n=10) of slots are employed. Hence $h_{10}=16$ mm, while the remaining optimized parameters are $\tau=0.8352$, and $p_9=8.4414$ mm. Thus the first slot has a height of $h_1=16\times0.8352^9=3.164$ mm, and $p_1=8.4414\times0.8352^8=1.999$ mm. The slot has a width of s=1.213 mm while the small gap at the vertex of each two slots is fixed as g=0.45 mm in this study. The occupied area of this demonstrator is found to be 40.65×29.546 mm².

4 Experimental characterization of the developed filter

The developed prototype filter was fabricated and experimentally characterized for the electric performance. Figure 3 shows the photograph of the developed filter. The measurement was carried out by a vector network analyzer (VNA) from Agilent technologies under a model number N9918A with full two-port calibrations.



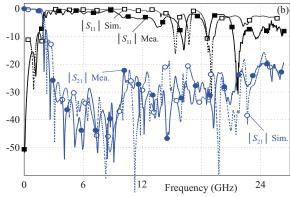


Fig. 4. Simulated and measured electric performance of the proposed log periodic DGS prototype filterainband responses bwide-band performance

Figure 4 shows the simulated and measured results for the fabricated prototype filter. It is found from measurements that the prototype filter has a cut-off frequency of approximately 1.96 GHz and for comparisons, the simulated response is also presented. The simulated cut-off frequency is about 2.14 GHz, as described in Fig. 4(a) for the in-band response. From DC to about 1.85 GHz, the insertion loss is better than 1 dB from measurements especially, it is less than 0.7 dB from DC to 1.54 GHz. Notice that the loss includes the conductor loss of the transmission line and that of the two SMA connectors at the two ends as well as the transitions between the connectors and the microstrip line. The measured in-band

		Passband	Sto	opband						
	f_c	insertion range (GHz		e (GHz)	Filter size (mm^2)			$\overline{\mathrm{DGS}}$		
Refs./year	(GHz)	loss	/suppression					patte	rn	Notes
		(dB)	leve	el (dB)						
(16)/2016	3.88	0.3	4.07 - 15/20		35.25×16.15		Dı	Dumbbell		Open stubs, spur-line,
(16)/2016	3.88	0.3	4.07 - 15/20		35.25×16.15		Dı	Dumbbell		defected microstrip line
(17)/2017	1.1	0.3	1.3 - 8.9/20		59.2×35.52		Dı	Dumbbell		Defected microstrip line,
(17)/2017	1.1	0.3	1.3 - 8.9/20		59.2×35.52		Dı	Dumbbell		compensated capacitor*
(18)/2018	2.4	0.35	2.8 - 6/20		20.86×11.92		M	Modified dumbbell		Split ring resonator
(19)/2019	2.2	1.9	2.7 - 12/50		10	100×2.6		Pi-shaped		Standard microstrip
(20)/2020	2.11	/	3-20/18		15.5×12.7		M	Meandered line		Spur line
(20)/2020	2.11	/	3-20/18		15.5×12.7		loa	loaded square		
(21)/2020	6	1.1	/		42×20		cir	circular		Standard
This work	1.96	0.7	2.61 26.5/20		40.65×29.546		Lo	Log periodic zigzag		microstrip
		Refs.	(16)	(17)	(18)	(19)	(20)	(21)	This wo	ork
		$arepsilon_{ m r}$	2.55	3.38	4.4	3.8	4.4	10.2	9.6	
		h (mm)	1.5	0.813	1.59	0.254	0.8	0.635	0.8	

Table 1. Comparisons of some related DGS filters and this study

group delay is 0.4 ns in general. Meanwhile, both simulation and measurement indicate that, the proposed filter has a sharp transition from the passband to the stopband.

The wideband suppression response is recorded in Fig. 4(b). It is measured from $2.61~\mathrm{GHz}$ to over $26.5~\mathrm{GHz}$, a high and wideband suppression is found under a suppression level of $20~\mathrm{dB}$.

Further, for performance comparisons, Tab. 1 presents the performance of the proposed filter and some reported works. it can be observed that the proposed filter has comparable good performance and an ultra-wide suppression band with a compact size. It should be noted that this is the first time to study and develop the log periodic zigzag fashion for potential applications to the microwave filters.

5 Conclusion

For the first time, the log-periodic zig-zag DGS has been exploited and proposed for microwave filter applications. Developing the proposed microwave filter is based on the design principles of the log-periodic antenna. The studied filter has been fabricated and experimentally characterized with promising performance, including low insertion loss in the passband and ultra-wide suppression band beyond the passband as well as sharp transition from the passband to the stopband. With these performances, the proposed filter topology is believed to be good potential for further integration with system applications.

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