A SLIDING MODE CONTROL FOR FOUR–WIRE SHUNT ACTIVE FILTER

Farid HAMOUDI* — Aziz CHAGHI*
Mouloud ADLI** — Hocine AMIMEUR*

The present paper deals with the sliding mode control of a three-phase four-wire shunt active filter SAF, to improve phase-current waveform, neutral current mitigation and reactive power compensation in electric power distribution system. The sliding mode is formulated using elementary differential geometry, then the control vector is deduced from the sliding surface accessibility using the Lyapunov stability. The algorithm used to establish the current references for the sliding mode controller is based on the instantaneous real and imaginary power theory for four-wire system. It will be seen that this method permits to synthesis the control vector with simple manner, and finally, the obtained simulation results confirm that the above objectives are satisfied.

**K e y w o r d s:** sliding mode control, four-wire active filter, harmonics current compensation

1 INTRODUCTION

The increased severity of harmonic pollution in power distribution network has attracted the attention to develop dynamic and adjustable solutions to the power quality problems giving rise to active filter [1–5]. Three-wire active filtering provides compensation of harmonics, reactive power, but can’t compensates zero sequence components caused by single-phase non-linear loads inherently generate more harmonics than three-phase non-linear loads, for this reason, four-wire active filtering is recommended in distribution system [3–5].

After synthesizing the reference currents, the voltage source inverter must inject these components in the point of common coupling with minimum error and fast response, this objective requires an appropriate current control method, in this regard, two different approaches are generally adapted; the fixed frequency control and the variable structure control (VSC). The first one requires a linear approach of the system to synthesize the control law. This approach can be advantageous for the fixed frequency but it is not very adapted to multi-frequencies signals. In fact the proportional integral controller performs traditionally for this approach are known for there handicap to regulate correctly alternating references.

The sliding mode control (SMC) which is derived from the theory of variable structure control introduced since long time [6], is a known discontinuous control technique which takes in account the time varying topology of the controlled system. Thus this technique is naturally suitable to control systems based on power electronics devices in general [7–9], and active filter as particular case of these systems [10,11]. It is characterized by simplicity implementation, high robustness in presence of uncertainty in system parameters, and fast response. For these reasons, the sliding mode control can be successfully applied to achieve harmonics references regulation.

The purpose of this paper is the application of the sliding mode control to a three-phase four-wire shunt active filter based on three-leg voltage source inverter (VSI).

The layout of this paper is as follows: The system description and the establishment of the differential equations of the system will make the object of the first part of this paper. After that, we will explain briefly the algorithm of the reference current computation based on the known pq–theory. The sliding mode control to active filter current control is then described in detail, and finally simulation results considering load variation will be given to measure the performances and the validity of the proposed control.

2 SYSTEM DESCRIPTION AND MODELING

Figure 1 illustrates the configuration of the four-wire shunt active filter under study in this paper. The power circuit is based on three-phase three-leg voltage source inverter, connected to the network through a first order passive filter \((L_C, R_C)\). At the DC side the bus is constituted of two capacitors \(C_1 = C_2 = C\) with a midpoint connected to the neutral wire of the network. The objective of this SAF topology is to inject a current vector \(i_{abcN}\) in the mains in order to compensate for harmonics, reactive and neutral components of the load currents, keeping the DC-link voltages \(V_{C1}\) and \(V_{C2}\) balanced and in an admissible range. To establish the dynamic equations of the system, let suppose that the power switches \(S_k\) can be assumed ideals, then the output voltage for each phase \(k\) to neutral can be expressed as follows

\[
v_{ch} = d_k V_{C1} - \bar{d}_k V_{C2}.
\]

(1)

Where the switch position functions \(d_k\) define the states of the power switches \(S_k\) and \(S'_k\) as follows

\[
d_k = \begin{cases} 
1 & \text{if } S_k \text{ on, } S'_k \text{ off}, \\
0 & \text{if } S_k \text{ off, } S'_k \text{ on}. 
\end{cases}
\]

Unauthentifiziert | Heruntergeladen 31.08.19 18:09 UTC
\[ d \text{ is defined as follows} \]

\[ \text{Note that} \]

\[ \text{scribed by the following differential equation} \]

\[ \text{Where} \]

\[ \text{Replacing the function} \]

\[ \text{and} \]

\[ \text{Putting} \]

\[ \text{which represent respectively the DC-bus voltage and the unbalance between the two capacitors. Replacing,} \]

\[ \text{Thus} \]

\[ u_k = \begin{cases} 
1 & \text{when} \ d_k = 1, \\
-1 & \text{when} \ d_k = 0. 
\end{cases} \]

\[ \text{With this new function, the power circuit in Fig. 1 can be replaced by an equivalent circuit as shown in Fig. 2, and then the equation (1) is rewritten as follows} \]

\[ u_{ck} = \frac{1}{2} u_{ck}(V_{C1} + V_{C2}) + \frac{1}{2}(V_{C1} - V_{C2}). \]

\[ \text{Putting} \]

\[ \text{Finally, these equations are rearranged under matrix from} \]

\[ \frac{\text{d}V_{C1}}{\text{d}t} = \frac{1}{2C} \left( \sum_{k=a,b,c} u_{k}i_{ck} - \sum_{k=a,b,c} i_k \right), \]

\[ \frac{\text{d}V_{C2}}{\text{d}t} = \frac{1}{2C} \left( \sum_{k=a,b,c} u_{k}i_{ck} + \sum_{k=a,b,c} i_k \right), \]

\[ L_o \frac{\text{d}i_{ck}}{\text{d}t} = -r_c i_{ck} + \frac{1}{2} u_{ck} V_{dc} + \frac{1}{2}(V_{C1} - V_{C2}) - e_k. \]
3 REFERENCE CURRENT IDENTIFICATION

The reference current identification is needed to synthesize the component to be compensated from the measuring the load current and the voltage at the PCC. In this paper, the pq-theory method is adopted to achieve this operation. This technique proposed in [12] is expanded and well explained in [3] for four-wire system applications. The principle of the pq-theory is to extract the compensated current components from the undesirable components in the instantaneous real, imaginary and zero-sequence powers. Consequently, if the mains voltages $e_{abc}$ are disturbed, the instantaneous power will not reflect correctly the load current status, for this reason a Fundamental Positive Sequence Identification (FPSI) is generally included. Thus, if we denote $e'_{α}, e'_{β}$, the instantaneous mains voltage corresponding to the correctly zero-sequence powers. Consequently, if the mains voltage contains any zero-sequence voltage component ($e_{γ} = 0$), as a consequence, zero-sequence power $p_{c}$ is also zero.

In ideal conditions, the optimal power flow is provided to the source when this one supplies only the average real power $\bar{p}$, this means that the active filter must compensate all other power components, and naturally the additional power components $p_{loss}$ and $Δp_{0}$ needed respectively to DC-bus voltage regulation and balancing, thus, the reference compensating powers are

$$p_{c}^{∗} = \bar{p} + p_{loss} + Δp_{0}, \quad q_{c}^{∗} = -q.$$  

(12)

These signals are then used to calculate the following reference current in $αβ$-frames

$$\begin{bmatrix}
   i_{ca}^{∗} \\
   i_{cb}^{∗} \\
   i_{cc}^{∗}
\end{bmatrix} = \frac{1}{\sqrt{2}} \begin{bmatrix}
   e_{α}' - e_{β}' \\
   e_{β}' \\
   e_{α}'
\end{bmatrix} \begin{bmatrix}
   \bar{p}_{c} \\
   q_{c}
\end{bmatrix}. \tag{13}
$$

(13)

Since the zero-sequence current must be compensated, the reference component in the $γ$-axis is $i_{γ}$, itself with negative sign evidently. Finally, the reference currents in $abc$-frames are obtained as follows

$$\begin{bmatrix}
   i_{ca}^{∗} \\
   i_{cb}^{∗} \\
   i_{cc}^{∗}
\end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix}
   \frac{1}{\sqrt{2}} & 0 & 1 \\
   \frac{1}{\sqrt{2}} & \frac{-\sqrt{3}}{2} & \frac{-\sqrt{3}}{2} \\
   \frac{1}{\sqrt{2}} & \frac{-\sqrt{3}}{2} & -\frac{-\sqrt{3}}{2}
\end{bmatrix} \begin{bmatrix}
   i_{ca}^{γ} \\
   i_{cb}^{γ} \\
   i_{cc}^{γ}
\end{bmatrix}. \tag{14}
$$

(14)

This algorithm is represented schematically in Fig. 3.

4 SLIDING MODE CONTROL

The sliding mode control consists to select the suitable switching configuration of the VSI in order to guarantee the state trajectory attraction toward a predefined sliding surface, and to maintain it stable over this surface.

The system established in (8) is a MIMO nonlinear system. In order to formulate the sliding mode creation problem, letting

$$x = [x_{1} \ x_{2} \ x_{3} \ x_{4} \ x_{5}]^{T} = [i_{ca} \ i_{cb} \ i_{cc} \ V_{c1} \ V_{c2}]^{T}.$$  

Then the state equation can be rearranged in the form

$$\dot{x} = f(x) + G(x)u.$$  

(15)

Where the $n$ dimensional vector field $f(x)$, the $n \times m$ dimensional input matrix $G(x)$ and the $m$ dimensional control vector $u$ are given as follows

$$f(x) = \begin{bmatrix}
   \frac{-\frac{x_{1}}{L_{c}}}{1} \ + \ \frac{x_{4}}{1} \ - \ \frac{x_{5}}{1} \\
   \frac{-x_{2}}{1} + \frac{x_{4}}{1} \ - \ \frac{x_{5}}{1} \\
   \frac{-x_{4}}{1} + \frac{x_{1}}{1} \ + \ \frac{x_{3}}{1} \\
   \frac{-x_{5}}{1} + \frac{x_{2}}{1} \ + \ \frac{x_{3}}{1} \\
   \frac{-x_{5}}{1} + \frac{x_{1}}{1} \ - \ \frac{x_{3}}{1}
\end{bmatrix} \equiv \begin{bmatrix}
   f_{1}(x) \\
   f_{2}(x) \\
   f_{3}(x) \\
   f_{4}(x) \\
   f_{5}(x)
\end{bmatrix},$$

(16)
G(x) = \begin{bmatrix} \frac{x_1 + x_5}{2} \\ \frac{x_2 + x_3}{2} \\ \frac{x_2 + x_2}{2} \\ \frac{x_1}{2} \\ \frac{x_2 + x_2}{2} \end{bmatrix} = \begin{bmatrix} g_1(x) \\ g_2(x) \\ g_3(x) \\ g_4(x) \\ g_5(x) \end{bmatrix}, \quad u = \begin{bmatrix} u_a \\ u_b \\ u_c \end{bmatrix}.

### 4.1 Sliding surfaces

For the n dimensional controlled system regulated by m independent switches, m sliding surface coordinate functions are defined. The m sliding surfaces are represented by the smooth algebraic restrictions \( \sigma_i(x) = 0, \ i = 1, 2, \ldots, m \). For each surface \( S_i \), we have

\[
S_i = \{ x \in \mathbb{R}^n \mid \sigma_i(x) = 0 \}. \tag{16}
\]

And the intersection of the m surfaces is denoted by \( S \) which verify

\[
S = \{ x \in \mathbb{R}^n \mid x \in S_i, \ i = 1, 2, \ldots, m \}. \tag{17}
\]

Let define the vector of the sliding surface coordinate functions as follows

\[
\sigma(x) = \begin{bmatrix} \sigma_1(x) \\ \sigma_2(x) \\ \sigma_3(x) \end{bmatrix} = \begin{bmatrix} x_1 - x_1^* \\ x_2 - x_2^* \\ x_3 - x_3^* \end{bmatrix}. \tag{18}
\]

We know yet that when the sliding mode is reached, in other words, when the state vector is forced to evolve on the intersection of the sliding surface, \( \sigma(x) \in S \), the sliding surface coordinate function \( \sigma(x) \) must satisfy the following condition

\[
(\dot{\sigma}(x), \sigma(x)) = (0, 0). \tag{19}
\]

Then, we can write

\[
\dot{\sigma}(x) = \frac{\partial \sigma(x)}{\partial x^T} (f(x) + G(x)u_{eq}(x)) = 0. \tag{20}
\]

We denote \( \frac{\partial \sigma(x)}{\partial x} f(x) \) by \( L_f \sigma(x) \), an dimensional vector which represents the direction derivative of \( \sigma(x) \) along the direction of the vector field \( f(x) \). Similarly, the \( m \times m \) dimensional matrix \( \frac{\partial \sigma(x)}{\partial x} \) is denoted by \( L_G \sigma(x) \). Thus, (20) is rewritten as follows

\[
\sigma(x) = L_f \sigma(x) + L_G \sigma(x) u_{eq}(x) = 0. \tag{21}
\]

This permits to define the equivalent control in the form

\[
u_{eq}(x) = -(L_G \sigma(x))^{-1} L_f \sigma(x). \tag{22}\]

This means that as a condition for the equivalent control definition is that the matrix \( L_G \sigma(x) \) must be invertible. Note also that the equivalent control must satisfy \(-1 \leq u_{eq}(x) \leq 1\) which is the necessary and sufficient condition for the sliding mode existence over the surface \( S \).

The expression of the equivalent control can be developed as follows

\[
u_{eq}(x) = -\left( \frac{\partial \sigma(x)}{\partial x} G(x) \right)^{-1} \left( \frac{\partial \sigma(x)}{\partial x} f(x) \right) \tag{23}\]

which permits to establish the equivalent control for the system as follows

\[
u_{eq}(x) = \begin{bmatrix} u_{eqa} \\ u_{eqb} \\ u_{eqc} \end{bmatrix} = \begin{bmatrix} \frac{1}{x_1 + x_2 + x_3} (2e_c x_1 - x_4 + x_5 + 2e_a) \\ \frac{1}{x_1 + x_2 + x_3} (2e_c x_2 - x_4 + x_5 + 2e_b) \\ \frac{1}{x_1 + x_2 + x_3} (2e_c x_3 - x_4 + x_5 + 2e_c) \end{bmatrix}. \tag{24}\]

Since the sliding mode is reached the state equation of the system is expressed as

\[
x = f(x) - G(x)(L_G \sigma(x))^{-1} L_f \sigma(x). \tag{25}\]

### 4.2 Sliding surface accessibility

Let consider the following Lyapunov function

\[
V(\sigma(x)) = \frac{1}{2} \sigma^T(x) \sigma(x). \tag{26}\]

It is a semi-definite function, it is identically zero over the surface \( S \), ie when \( \sigma(x) = 0 \) and positive when \( \sigma(x) \neq 0 \). The quantity \( V(\sigma(x)) \) can be interpreted as the distance from the position of the point \( x \) in the state space to the desired surface \( S \). Therefore, in order to satisfy the condition \( \sigma(x) \neq 0 \), the discrete control \( u \) must exercise a closing or opening action, which permits to decrease the distance \( V(\sigma(x)) \), this means that the variation of this function in the time must be strictly negative, then

\[
\frac{d}{dt} V(\sigma(x)) = \sigma^T(x) \dot{\sigma}(x) < 0. \tag{27}\]

This is the condition for the trajectory attraction toward the sliding surface.

Referring to (21) and (27), if \( \sigma(x) \neq 0 \), replacing \( u_{eq}(x) \) by \( u \), then the time derivative of the Lyapunov function can be expressed as follows

\[
\dot{V}(\sigma(x)) = \sigma^T(x)(L_f \sigma(x) + L_G \sigma(x) u) < 0. \tag{28}\]

Likewise, if \( \sigma(x) = 0 \), then

\[
\dot{V}(\sigma(x)) = \sigma^T(x)(L_f \sigma(x) + L_G \sigma(x) u_{eq}) = 0. \tag{29}\]

Now, if we consider that the switching frequency is infinite or sufficiently high, we can suppose with good approximation that the state vector \( x \) takes the same value in the both case (28) and (29). Thus, subtracting (29) from (28), the restriction (27) can be rewritten as follows

\[
\dot{V}(\sigma(x)) = \sigma^T(x)L_G \sigma(x) (u - u_{eq}(x)) < 0. \tag{30}\]

This inequality can be achieved by applying the control vector given by

\[
u = -\text{sign}(\sigma^T(x)L_G \sigma(x))^T. \tag{31}\]

And finally, the switch position functions

\[
d = \frac{1}{2} (1 - \text{sign}(\sigma^T(x)L_G \sigma(x))^T). \tag{32}\]

Where sign designs the sign function and \( \textbf{1} \) is an \( m \) dimensional column vector constituted of 1 in each entry.
5 SIMULATION RESULTS

The performances of the developed sliding mode control were verified through simulation using MATLAB software. The polluting load is constituted with three-phase thyristor rectifier, single-phase thyristor rectifier and single-phase diode rectifier. The resulting load currents are thus deformed and unbalanced. The mains parameters of the system are

Phase-to-neutral voltage source 230 V rms, 50 Hz

Source inductance $L_s = 100 \mu\text{H}$
DC-bus capacitors $C_1 = C_2 = 5 \text{ mF}$
DC-bus voltage reference $V_{dc}^* = 1000 \text{ V}$
Inductor filter $L_c = 2 \text{ mH}$

The mains simulation results are illustrated in the following figures. First, in Fig. 4 it can be seen that the three-phase and the neutral currents are correctly compensated. In fact, the spectrum analysis in Fig. 5 shows that without active filtering, the mains current contain
positive sequence harmonics \(6h + 1\) \((7^{th}, 13^{th}, \ldots)\), negative sequence harmonics \(6h - 1\) \((5^{th}, 11^{th}, \ldots)\), and zero sequence harmonics \(6h - 3\) \((3^{rd}, 9^{th}, \ldots)\), with \(h \geq 1\) designs the harmonic order. Note that the zero sequence harmonics is the result of the 4\(^{th}\)-wire (neutral), this sequence does not appears in the three-wire systems. With introducing active filtering action, these components are almost canceled.

Figure 6 illustrates the performances of the active filter controller to guarantee source current in sinusoidal form and in phase with mains voltage, under load change, where it can be seen that the power factor and the total harmonic distortion are both excellently improved before and after load change. This can be observed also in Fig. 7 where the instantaneous real and imaginary power is represented. The imaginary power is almost canceled, and the real power drawn from the AC source is practically free of the alternating part of the load power. The real power spectrum in Fig. 7(b) shows effectively that the 2\(^{nd}\)-harmonic order components in the load power are mitigated in the source side. This quality is also observed after load change as it is illustrated in Fig. 7(c).

### Table 1. Detailed results of three-phase and neutral currents compensation

<table>
<thead>
<tr>
<th></th>
<th>3-phase, Neutral</th>
<th>(t &lt; 0.2) s</th>
<th>(t &gt; 0.2) s</th>
</tr>
</thead>
<tbody>
<tr>
<td>THD (%)</td>
<td></td>
<td>Load</td>
<td>Source</td>
</tr>
<tr>
<td>(a) phase</td>
<td>28.29</td>
<td>01.60</td>
<td>26.15</td>
</tr>
<tr>
<td>(b) phase</td>
<td>27.67</td>
<td>01.45</td>
<td>24.83</td>
</tr>
<tr>
<td>(c) phase</td>
<td>23.76</td>
<td>01.76</td>
<td>23.59</td>
</tr>
<tr>
<td>Neutral</td>
<td>31.31</td>
<td>–</td>
<td>31.31</td>
</tr>
<tr>
<td>RMS</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(a) phase</td>
<td>54.03</td>
<td>54.34</td>
<td>98.42</td>
</tr>
<tr>
<td>(b) phase</td>
<td>58.74</td>
<td>54.73</td>
<td>105.57</td>
</tr>
<tr>
<td>(c) phase</td>
<td>65.21</td>
<td>54.44</td>
<td>108.57</td>
</tr>
<tr>
<td>Neutral</td>
<td>13.10</td>
<td>–</td>
<td>13.10</td>
</tr>
</tbody>
</table>

### Table 2. Detailed results of instantaneous real and imaginary powers compensation

<table>
<thead>
<tr>
<th></th>
<th>3-phase, Neutral</th>
<th>(t &lt; 0.2) s</th>
<th>(t &gt; 0.2) s</th>
</tr>
</thead>
<tbody>
<tr>
<td>THD (%)</td>
<td></td>
<td>Load</td>
<td>Source</td>
</tr>
<tr>
<td>(p)</td>
<td>15.98</td>
<td>03.66</td>
<td>16.85</td>
</tr>
<tr>
<td>(q)</td>
<td>107.6 (\times 10^4)</td>
<td>03.71</td>
<td>06.64</td>
</tr>
<tr>
<td>DC component</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(p)</td>
<td>03.80</td>
<td>03.71</td>
<td>06.64</td>
</tr>
<tr>
<td>(q)</td>
<td>01.29</td>
<td>–</td>
<td>02.58</td>
</tr>
</tbody>
</table>

Tables 1 and 2 summarize the detailed results of current and instantaneous power compensation. Note that the total harmonic distortion of the real power is computed to DC component.

The DC-bus voltage regulation is shown in Fig. 8 the DC-bus is charged and the voltage level \(V_{dc}\) reaches the predefined value \(V_{dc}^*\) with excellent response \((950\, \text{V} < V_{dc} < 1050\, \text{V} \text{ in } 0.05\, \text{s})\). The excellent dynamic performance is also observed with load change operated at \(t = 0.2\, \text{s}\), in fact the DC-link voltage is remained inside
the bounded range \([950 \text{ V}, 1050 \text{ V}]\) and stabilized at \(V_{dc}^*\) in 0.1 s. The DC-link unbalance is shown in Fig. 8(c) where one can remark that the two voltage \(V_{C1}\) and \(V_{C2}\) are sufficiently balanced, accept the existence of small oscillation \(\Delta V_{dc}\) (about 1.3 % of \(V_{dc}\)) which can be neglected. It should be noted that the DC-link voltage oscillation \(V_{dc}\) and \(\Delta V_{dc}\) are strongly depending on the capacitors value. In fact, it is known that to keep these oscillation inside an acceptable bounded ranges, generally a large capacitors is recommended, the two capacitors increase considerably when \(C_1\) and \(C_2\) are chosen small, for this reason, in this paper this parameters is relatively large.

6 CONCLUSION

In this paper, a sliding mode control for three-phase three-leg voltage source inverter based four-wire shunt active filter is applied. The system is observed as a MEMO-decoupled nonlinear system, which permits a simple implementation. The simulation results show the ability of this method to track references with minimum error, fast response and high robustness. A low line current THD, and high power factor are provided even load change.

Finally, as critics, it should be noted that, the decoupled nature of the used VSI topology is simple to analyze but we have seen that it need very large capacitors value in the DC-bus.

For the sliding mode control, it is known that the switching frequency presents its principal incontinence. The replacement of the three-leg topology by the four-leg one can resolve the problem of first mentioned critic, and we know yet that the switching frequency stabilization is possible. This will make the object of the continuation for this work.

REFERENCES


Received 18 October 2009

Farid HARMoudi was born in Bejaia, Algeria in 1980. He received the engineer diploma from the University of Bejaia, and the M.S degrees from the University of Batna in electrical engineering in 2005 and 2008 respectively. He is actually working toward the PHD thesis. His area of research includes power electronics, harmonics in power systems and active harmonics filtering. From December 2009, he is an assistant teacher at the Institute of Electrical Engineering and Electronics, at the University of Boumerdes, Algeria.

Abdelaziz CHAGHI was born in Batna, Algeria, 1954. He received his BS degree from the University of Oran, Algeria 1980, and Master degree from the University of Manchester, England 1984, and received his PhD from Batna University, Algeria 2004. He is currently a Professor at department of Faculty of Technology, Electrical Engineering at Batna University. His areas of interest include power systems optimization, power system protection and power quality.

Mouhoud ADLI was born in Tizi Ouzou, Algeria in 1957. He received the engineer diploma in electrical engineering from the Ecole Nationale Polytechnique, Algiers, Algeria in 1984 and the Doct. Ing diploma from the T.U. University of Magdeburg, Germany in 1993. He is now working as assistant professor and a researcher in the Electrical Engineering Research Laboratory at the University of Bejaia. His areas of interest include fuzzy control, harmonics in power systems and power electronics.

Hocine AMIMEUR was born in Bejaia, Algeria in 1977. He received the engineer diploma from the University of Bejaia, and the M.S degrees from the University of Batna in electrical engineering in 2005 and 2008 respectively. He is actually working toward the PHD thesis. His area of research includes power electronics, induction machines and renewable energies. From February 2011, he is an assistant teacher at the Electrical Engineering Department, at the University of Bejaia, Algeria.