# Scanning capacitance microscopy characterization of AIIIBV epitaxial layers\*

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The applicability of scanning capacitance microscopy (SCM) technique for chosen electrical properties characterization of AIIIBV structures fabricated by Metalorganic Vapor Phase Epitaxy (MOVPE) was examined. The calibration curves for quantitative characterization of doping levels in GaAs layers were created. The AlGaN/GaN/Si heterostructures for high electron mobility transistor fabrication and InGaAs tunnel junction for tandem solar cell characterization were presented. The crucial factors of measurement conditions which could influence the obtained results were also discussed.

Keywords: scanning capacitance microscopy (SCM); atomic force microscopy (AFM); GaAs; InGaAs; AlGaN/GaN

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# 1. Introduction

Continuous decrease of the characteristic dimensions of active regions of semiconductor devices is a driving force for developing advanced atomic force microscopy (AFM) techniques as scanning spreading resistance microscopy (SSRM), scanning surface potential microscopy (SSPM) and scanning capacitance microscopy (SCM) which allow us to characterize electrical parameters with nanometer spatial resolution [1-3]. In SCM, the conductive tip during imaging in contact mode is biased by alternating voltage. When the tip is scanning a semiconductor sample covered by a thin insulator layer, it forms a metal-insulator-semiconductor structure whose capacitance change induced by alternating voltage depends on free carrier concentration in the semiconductor. As a result, by measuring dC/dV signal, it is possible to obtain information about the type and carrier concentration distribution with nanometer resolution. Although SCM method is successfully used for silicon structures qualitative and quantitative characterization [4, 5], its application in AIIIBV compounds investigation is restricted by the lack of reliable and well documented measurement methodology. In this paper the crucial aspects of obtaining proper SCM measurement conditions for GaAs, In-GaAs and AlGaN/GaN/Si device structure characterization were discussed and the results presented.

## 2. Experimental

The measurements were performed using Veeco Multimode V atomic force microscope equipped with scanning capacitance microscopy mode setup. The detailed description of SCM measurement principles could be found in [6]. Here, it is worth mentioning that the measurement output – SCM signal – is defined as dC/dV amplitude changes for the flat band condition signed by the slope of the dC/dV curve. Three types of AIIIBV samples were measured, all grown by metalorganic vapor phase epitaxy (MOVPE): GaAs structure with the layers of different n doping levels, InGaAs tunnel junction for tandem solar cells and high electron mobility transistor AlGaN/GaN/Si heterostructures. More information about the growth process parameters,

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construction and properties of each of those structures can be found in the literature [7–9]. For each type of the samples, the SCM cross section profiles and maps were obtained by scanning their surfaces cleaved and covered with a thin insulating layer of native oxide (created by their exposition to air). To make electrical contact to the samples, a GaIn eutectic mixture was used.

There are three major components which influence mostly SCM imaging: stable mechanical AFM tip-sample surface contact (I), optimized parameters of SCM measurement (II) and selection of AFM tip model (III).

(I) Setting a proper mechanical contact of AFM tip with the sample surface is necessary to obtain steady and repeatable electrical contact at different points of examined structure. This is achieved by adjusting the contact mode topography imaging parameters: AFM tip press force (too small value may result in loosing the contact, too high may lead to tip damage) as well as feedback loop parameters and scanning speed (it has to be optimized to maintain constant tip press force at different locations of the sample).

(II) The SCM mode parameters (AC voltage amplitude (V<sub>AC</sub>) and frequency, capacitance sensor frequency and DC bias voltage (V<sub>DC</sub>) of sample) must be adjusted to obtain the maximal value of measured signal. The sample DC bias should be chosen carefully because too high values (from our experience this is the case of  $V_{DC} > 5$  V) could induce some charge localized in the surface states of the semiconductor sample which, in consequence, distorts the obtained results [10]. Another source of the experimental results distortion can be introduced by stray light in the measurement chamber which, if consists of photons with a sufficiently high energy, may induce photo-carriers in the sample [11]. So, any influence of illumination (for example from laser beam in a cantilever deflection control system) on the obtained results must be also monitored.

(III) Selection of AFM tip is a crucial factor on which final quality of measurement results depends mostly. Firstly, in our research the test measurements were performed to select

AFM tip type which would be used in further experiments. Five different types being a good representation of conductive tips available currently on the market have been tested for fulfillment the following requirements: high SCM image resolution and contrast, good measurement stability and long AFM probe lifetime. The conclusions of the tests were as follows: the largest lifetime had the tip with the conductive diamond coating (DDESP-FM/NANOSensors) but, due to relatively large radius of the tip, the images resolution and contrast were poor. The best images quality was obtained using Cr/Pt (MULTI75E-G/BudgetSensors) and Pt/Ir (SCM-PIT/NANOSensors) coated tips but the second one guaranteed better stability and longer usage. The gold covered AFM tip (PPP-NCSTAu/NANOSensors) had a very short lifetime due to very low hardness of this metal, while the images obtained using Co/Cr (MESP/Bruker) coated tip presented insufficient resolution. Based on these preliminary observations, all measurements, which results will be presented in the next section, were performed using Pt/Ir coated tips with a scanning speed of 1 µm/s and force applied to the tip of about 200 nN (both the values were optimized for good imaging stability with sufficient lifetime of the AFM tip).



Fig. 1. Schematic cross section of the test GaAs multilayer sample.

### 3. Results

### 3.1. n-type doped GaAs multilayer

The GaAs samples were prepared by MOVPE epitaxy and consisted of four 250 nm thick



Fig. 2. E-CV (a) and SCM (b) profile of the GaAs test sample.

n-type layers with different carrier concentration (A:  $n = 2.5 \times 10^{18} \text{ cm}^{-3}$ , B:  $n = 2.0 \times 10^{17} \text{ cm}^{-3}$ , C:  $n = 3.0 \times 10^{16} \text{ cm}^{-3}$ , D:  $n = 9.5 \times 10^{17} \text{ cm}^{-3}$  and E: insulating substrate  $n \sim 1 \times 10^{16} \text{ cm}^{-3}$ ) deposited on an insulating substrate (Fig. 1). Such structures were fabricated to verify whether the SCM method could be used for quantitative characterization of doping level concentration in GaAs layers by creating calibration curves allowing to correlate the obtained SCM signal with carrier concentration magnitude.

Fig. 2a presents the profile of carrier concentration in the GaAs test sample measured by electrochemical CV method (E-CV) (the respective layers are marked on the top). Relevant SCM signal profiles obtained for  $V_{AC} = 2 V$  and two different DC sample bias voltages ( $V_{DC} = 0 V$  and 2 V) are presented in Fig. 2b. The resulting calibration curves of the SCM signal in dependence on carrier density are presented in Fig. 3. It could be noticed that for both polarization voltages V<sub>DC</sub>, the calibration curves have monotonic character and allow to unambiguous determining carrier concentration based on SCM measurement. This is a clear advantage of SCM technique over SSRM method where similar calibration curves determined for the same sample have large variability which excludes its application for quantitative characterization of GaAs layers [12].



Fig. 3. Calibration curves of SCM measurement.

# **3.2.** InGaAs tunnel junction for tandem solar cells

Fig. 4 presents a scheme of InGaAs tunnel junction structure intended for tandem solar cells. In the device designing process and layers growth controlling it is crucial to determine the exact position of p-n electrical junction. Both E-CV and SCM methods can be used for this purpose. However, as can be seen in Fig. 5, the electrical junction position can be determined more precisely using SCM measurements. Comparison of profiles presented in the Fig. 5 shows that the electrical junction position is more precisely localized based on SCM measurement for which a sharp transition from negative signal (p type doping) to positive (n type doping) can be easily seen. The SCM mode parameters ( $V_{DC} = 0.5$  V and  $V_{AC} = 1$  V) were selected to obtain higher difference of measured signal levels from p and n type doped layers. Because they differ from the parameters used during imaging of n-GaAs test sample, the calibration curves presented in Fig. 3 could not be directly applied for quantitative characterization of this structure.



Fig. 4. Schematic cross section of InGaAs tunnel junction sample.

### 3.3. AlGaN/GaN/Si heterostructures

Schematic view of a high electron mobility transistor based on AlGaN/GaN heterostructure grown on silicon substrate is presented in Fig. 6. Due to the difference in thermal expansion coefficient and lattice mismatch between epitaxial layer and substrate, the growth of such structure with a quality required for proper device operation is not a trivial task [13]. Basing on determined calibration curves (Fig. 3) SCM was used to verify the expected carrier concentrations levels (very high in transistor channel at AlGaN/GaN interface and very low in the rest of the structure) across the investigated structure and also its distribution uniformity for the respective layers. Fig. 7 presents the topography profile of the sample cross section and the SCM signal profile of AlGaN/GaN/Si structure with depicted position of each layer (to prevent the AFM tip from slipping from the semiconductor layer edge, a photoresist layer was deposited on the top of the structure, which was also included in the topography). The most interesting observation is the existence of high carrier concentration not only at the AlGaN/GaN interface but also in the region of LT (low temperature): AlN. This means that a parasitic channel for electrons exists in the structure which may result in significant deterioration of transistor parameters [14].

# 4. Summary

It was shown that SCM method can be used for semiconductor structures characterization of various AIIIBV compounds. Some factors and measurement parameters which could influence the obtained results were also described. For GaAs multilayers with different doping concentration the adequate calibration curves were obtained, which allowed quantitative carrier concentration imaging in this material. In the case of epitaxial AlGaN/GaN/Si heterostructures with a double GaN buffer layer, the spatial uniformity of electrical parameters was examined, in particular, the existence of a second electron conduction channel was revealed. It was also shown that in the case of InGaAs based tunnel junction, the SCM method can be successfully applied and the valuable information about the exact position of the junction can be quickly obtained.

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Fig. 5. E-CV (a) and SCM (b) profiles of InGaAs tunnel junction sample.



Fig. 6. Schematic cross-section of AlGaN/GaN/Si heterostructure.

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Fig. 7. Topography profile (blue) and SCM signal scan (red) of AlGaN/GaN/Si heterostructure.

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