

Analysis of electromigration phenomenon in thick-film and LTCC structures at elevated temperature*

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Studies on electromigration phenomenon in thick-film structures on alumina and LTCC substrates are presented in this paper. The effects of storage of Au and Ag electrode patterns in temperature range up to 300 °C under voltage bias were examined. The leakage characteristics of electrodes with 100 μm spacing at 50 V dc bias as a function of time and temperature are presented and analyzed. Scanning electron microscope (SEM) equipped with the energy-dispersive X-ray spectroscopy (EDX) detector was applied for determination of metal ions transport. Test structures with Au-based conductive material are much more resistant to electromigration than Ag-based layers.

Keywords: *LTCC; thick-film; electromigration; reliability*

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1. Introduction

Consumer electronics with typical operating environments of -40 °C to $+85\text{ °C}$ is the largest electronics market segment. A small group of specialized components complies with requirements for application in the temperature range from -55 °C to $+125\text{ °C}$. However, an increasing interest in the field of high temperature electronics has been observed for recent years [1] and there are applications for electronics that operate at much higher temperatures. These include: distributed controls for automotive and aircraft, oil and gas exploration and production, industrial processes control or even space exploration [2, 3]. Thick-film and LTCC (Low Temperature Cofired Ceramics) technologies are well-established fabrication methods of microelectronic circuits. They utilize conductive pastes to interconnect different discrete device on a common substrate. However, a metal inclusion component in conductive pastes might be susceptible to electromigration phenomenon that affects

reliability of such electronic circuits [4–6]. The metallic electromigration is defined as the movement of metallic material, usually through or across a nonmetallic medium, under the influence of an electrical field. Two basic types of electromigration are observed. The first one is an electrochemical (ionic) migration that takes place under normal ambient conditions in presence of moisture on a surface. The second type is solid-state electromigration that becomes important at local temperatures above 150 °C and at high current densities. Thus, high temperature exploitation of thick-film and LTCC circuits requires recognition of potential failure processes. This paper presents characterization of the effects of high temperature storage under dc bias, which occur in thick-film conductor patterns made of commercially available thick-film and LTCC materials.

2. Test structures

The test structures used for investigation of electromigration were made using a combination of standard screen-printing with laser shaping [7, 8]. The electrodes were made on alumina or LTCC (DP951 tape, DuPont) substrates. The four different electrode shapes (Fig. 1) were created by

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laser cutting of Ag-based (DP6145, DuPont) or Au-based (ESL8844, ElectroScience Lab.) conductive layers. The minimal distance between electrodes was equal to 100 μm . Frequency-tripled Nd:YAG laser (wavelength of 355 nm) – ProtoLaser U system (LPKF, Germany) was used for this purpose. Next the samples were fired in a belt furnace with a standard temperature profile ($T_{max} = 850\text{ }^{\circ}\text{C}$, total firing time $t = 60$ minutes).

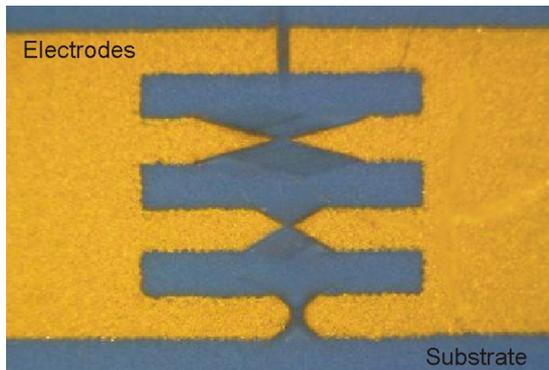


Fig. 1. Test structures with different shapes of electrodes (gold color – conductive material, blue – substrate).

3. Investigation and results

The fired structures have been observed with Hitachi SU6600 scanning electron microscope (Fig. 2 and 3) and AxioCam MR5 (Zeiss) optical microscope. The grain structure with many pores is visible. Both Au and Ag grains have dimensions of several micrometers. Additionally, an EDX (Energy-Dispersive X-ray Spectroscopy) was performed on chosen areas of the structure. The elemental analysis of the samples exhibited presence of standard materials used in thick-film composition (Table 1) i.e. metal (Ag, Au) from active phase and aluminium, oxygen, silicon or lead, mainly from glass phase but also probably from the substrates. One should be noticed that the presence of Na and Ca ions especially in substrate material might be a reason for higher leakage current at elevated temperature.

A very good removal of conductive material was observed by laser processing. However, a

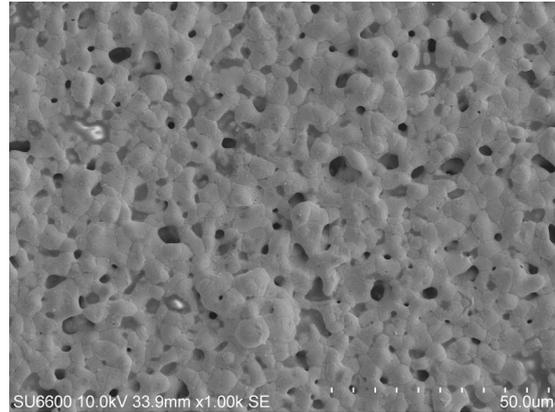


Fig. 2. Scanning electron micrograph of thick-film silver layer (magnification 1000 \times).

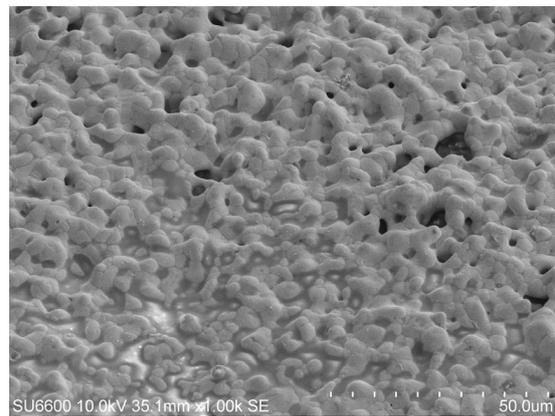


Fig. 3. Scanning electron micrograph of thick-film gold layer (magnification 1000 \times).

clearly visible damage of substrate material occurred due to laser ablation, both on alumina and LTCC substrate (Fig. 4).

After microstructure analysis the test structures have been subjected to thermal ageing process. The samples were placed on a hot plate (Microtherm SHP-500) equipped with a PID temperature controller. Each structure was wired to laboratory power supply through a series resistor $R = 100\text{ k}\Omega$ (Fig. 5) in order to limit the current in case of shorted structure as well as to allow monitoring the leakage current for each pattern. The Agilent 34970A multimeter was used for measurements of voltage drop across the series resistor. The measurements were carried out consecutively at 200 $^{\circ}\text{C}$ and 300 $^{\circ}\text{C}$ for about 160 and 250 hours under

Table 1. Elemental analysis of conductive films (wt.%).

	Ag	Au	O	Al	Si	Na	Ca	Pb
DP6145/LTCC	89.47	–	6.96	0.57	1.37	0.23	0.25	–
ESL8844/LTCC	–	89.79	2.60	0.43	–	0.53	–	2.52

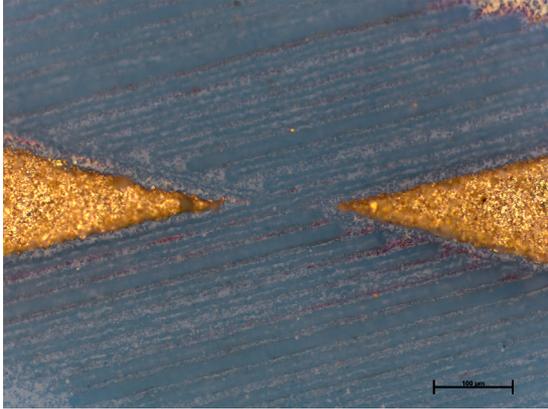


Fig. 4. Gold conductor on LTCC substrate (magnification 100 ×).

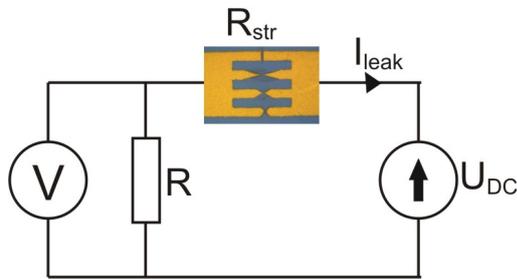


Fig. 5. Scheme of the test set-up for leakage current measurements.

50 V dc bias. Data were collected every 10 minutes. Fig. 6 presents the changes in leakage current for Ag-based test structures at different temperatures, whereas the resistance of investigated structures, calculated from the formula $R_{str} = U_{DC}/I_{leak} - R$, is shown in Fig. 7. After 106 hours at 200 °C a significant increase of leakage current was observed for silver electrodes on alumina substrate. At temperature of 300 °C an increase of current to 420 μA just after 3 hours was observed for silver on LTCC substrate. Within next 4 hours it raised up to 470 μA.

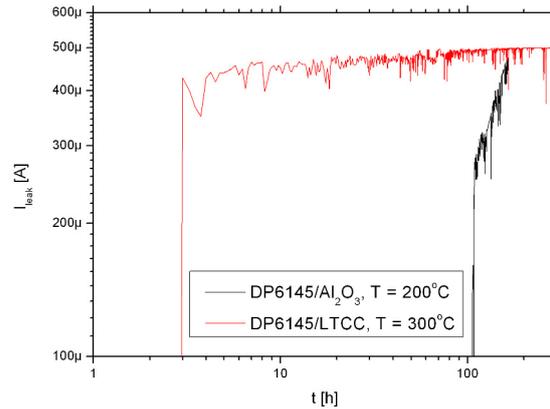


Fig. 6. Leakage current between Ag-based electrodes vs. time at different temperatures.

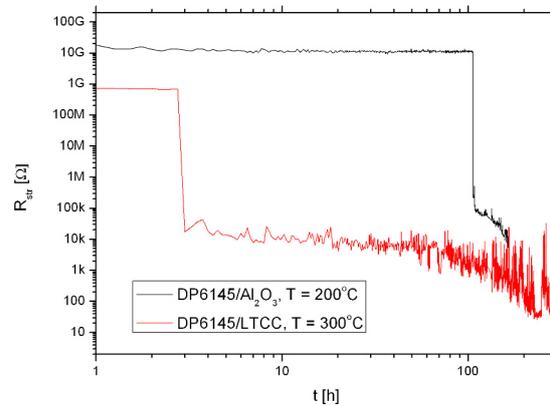


Fig. 7. Resistance of the structure vs. time at different temperatures.

No changes in leakage current under presented test conditions have been observed for structures with gold electrodes. Table 2 presents comparison of leakage current tests.

4. Failure analysis

Scanning electron microscope has been applied again for determination of failure mechanism in the

Table 2. Comparison of leakage current test results.

Structure	Test conditions	Leakage current
DP6145/Al ₂ O ₃	U = 50 V, T = 200 °C, t = 160 h	$I_{(t=106h)} = 140 \mu\text{A}$ ↗ $I_{(t=110h)} = 430 \mu\text{A}$
DP6145/LTCC	U = 50 V, T = 200 °C, t = 160 h	$I < 1 \mu\text{A}$
DP6145/LTCC	U = 50 V, T = 200 °C, t = 160 h + U = 50 V, T = 300 °C, t = 250 h	$I_{(t=3h)} = 420 \mu\text{A}$ ↗ $I_{(t=7h)} = 470 \mu\text{A}$
ESL8844G/Al ₂ O ₃	U = 50 V, T = 200 °C, t = 160 h + U = 50 V, T = 300 °C, t = 250 h	$I < 1 \mu\text{A}$
ESL8844G/LTCC	U = 50 V, T = 200 °C, t = 160 h + U = 50 V, T = 300 °C, t = 250 h	$I < 1 \mu\text{A}$

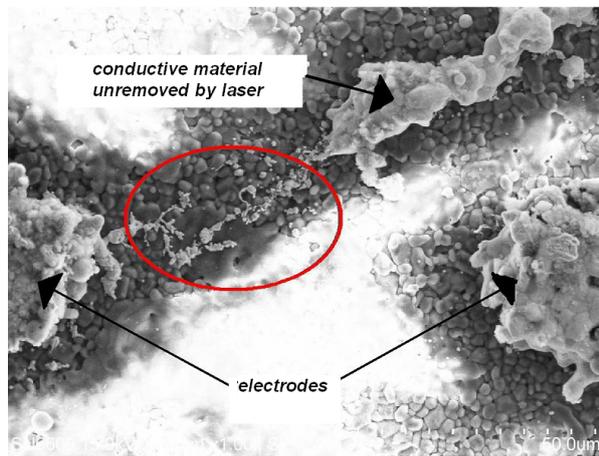
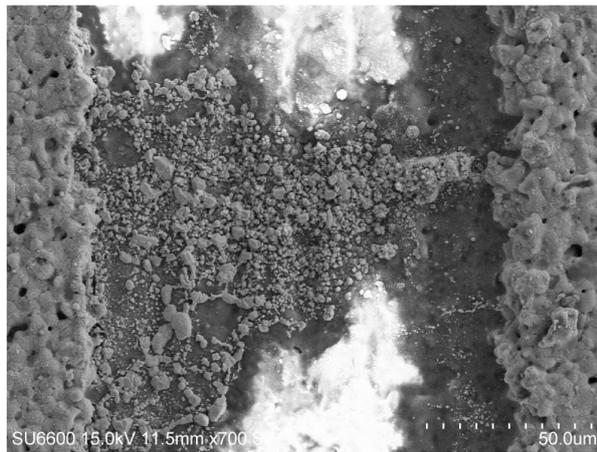
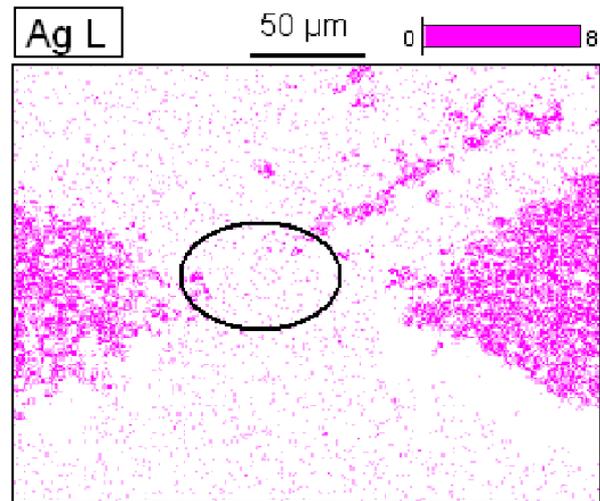
Fig. 8. Conductive path between silver electrodes on Al₂O₃ substrate.

Fig. 9. Conductive path between silver electrodes on LTCC substrate.

Fig. 10. Silver electrodes on Al₂O₃ substrate (EDX mapping).

test structures. The micrographs made previously were compared to those after ageing tests. In Fig. 8 and 9 scanning electron micrographs of the samples are presented where an increase of leakage current had occurred. In both of them a conductive path was created due to solid-state electromigration process i.e. transport of material caused by the gradual movement of the ions in a conductor due to the momentum transfer between conducting electrons and diffusing metal atoms. The mean time to failure for electromigration process is modelled as

$$MTTF = \frac{A}{j^n} \exp\left(\frac{E_a}{kT}\right) \quad (1)$$

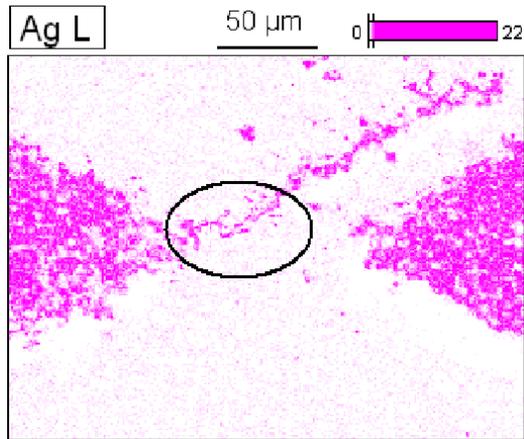


Fig. 11. Silver electrodes on Al_2O_3 substrate after ageing at 200 °C under 50 V dc bias (EDX mapping).

where A is a constant which comprises the material properties and the geometry of the interconnect, j is current density, n is a model parameter for current density, E_a is the activation energy, k is Boltzmann's constant, and T is the temperature [9]. The current density is the largest for V-shaped electrodes, therefore electromigration appears faster near these electrodes (Fig. 8). After creation of first shortcut connection between electrodes the leakage current intensity increased causing even more effective transport of ions.

EDX analysis has been used to determine the presence of mobile ions and the elemental analysis confirmed that conductive paths have been created by silver ions. Element maps before and after ageing test are presented in Fig. 10 and 11.

5. Conclusions

Two thick-film conductors have been evaluated for 300 °C applications. High temperature biased ageing identified a failure mechanism. A creation of conductive path (shortcuts) due to solid-state electromigration process has been observed in case of silver-based films. Temperature level had a significant influence on intensity of ions transport.

Acknowledgements

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References

- [1] JOHNSON R., EVANS J.L., JACOBSEN P., THOMPSON J.R. CHRISTOPHER M., *IEEE T. Electron. Pa. M.*, 27 (2004), 164.
- [2] NEUDECK P., OKOJIE R., CHEN L.-Y., *P. IEEE*, 90 (2002), 1065.
- [3] BUTTAY C. et al., *Mat. Sci. Eng. B-Solid*, 176 (2011), 283.
- [4] NAGUIB H., MACLAURIN B., *IEEE T. Compon. Hybr.*, 2 (1979), 196.
- [5] KRUMBEIN S.J., *IEEE T. Compon. Hybr.*, 11 (1989), 5.
- [6] ZHANG R., JOHNSON R.W., VERT A., ZHANG T., SHADDOCK D., *IEEE T. Compon. Hybr.*, 2 (2012), 1750.
- [7] MIŚ E., BORUCKI M., DZIEDZIC A., KAMIŃSKI S., WOLTER K.-J., SONNTAG F., *ESTC 2006*, 954.
- [8] NOWAK D., MIŚ E., DZIEDZIC A., KITA J., *Microelectron. Reliab.*, 49 (2009), 600.
- [9] ORIO R., CERIC H., SELBERHER S., *Microelectron. Reliab.*, 52 (2012), 1981.

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